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METHOD AND SYSTEM FOR PROVIDING TRANSPORT OF CHANNELIZED CIRCUITS OF ARBITRARY BIT RATE THROUGH ASYNCHRONOUS TRANSFER MODE (ATM) CIRCUIT EMULATION SERVICE (CES)

ABSTRACT OF THE DISCLOSURE

A system for managing circuit emulation service over an Asynchronous Transfer Mode (ATM) network is provided. The system includes control logic configured to receive channelized circuit data from a client, the channelized circuit data being transmitted at an arbitrary rate. The channelized circuit data is formatted into one or more ATM cells. Each ATM cell has a payload. The payload has a number of octets and corresponding validity fields. Each validity field is used to indicate whether the associated octet contains valid data. The one or more ATM cells are transmitted across the ATM network. By examining the validity fields, the transmission of the one or more ATM cells effectively results in transmission of the channelized circuit data at the arbitrary rate over the ATM network. The arbitrary rate is not a multiple of a fundamental rate.

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